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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

21125

APPL NUM 10064051	FILING DATE 06/05/2002	CLASS 257	SUBCLASS 592	GAU 2811	EXAMINER 285 <i>John Lee</i> LANDAN
**APPLICANTS: Chen Anchor;					
**CONTINUING DATA VERIFIED:					
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** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no				ATTORNEY DOCKET NO	
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no				NAUP0486USA	
Verified and Acknowledged Examiners's initials					
TITLE : Bipolar junction transistor and fabricating method					
<small>U.S. DEPT. OF COMM./PAT. & TM.-PTO-435L (Rev. 12-94)</small>					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
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